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## FACSIMILE COVER SHEET

**DATE:** April 3, 2007

**TO:** Examiner TSANG, Fan S. **FAX NO.:** 571-273-8300  
USPTO GPAU 2161

**FROM:** Jeffrey G. Toler  
Reg. No.: 38,342

**RE U.S. App. No.:** 10/613,656, filed July 3, 2003

**Applicant(s):** Darius Mostowfi, et al.

**Atty Dkt No.:** 1970-0005C

**Title:** System and Method for Using Multiple Communication Protocols in  
Memory Limited Processors

**NO. OF PAGES (including Cover Sheet):** 3

### MESSAGE:

Attached please find:

- Transmittal Form (1 pg)
- Request for Status of Application (1 pg)

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NO. 036 P. 2

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PTO/SB/21 (09-06)

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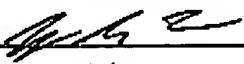
3

Application Number	10/613,656
Filing Date	July 3, 2003
First Named Inventor	Darius Mostowfi, et al.
Art Unit	2161
Examiner Name	TSANG, Fan S.

Attorney Docket Number 1970-0005C

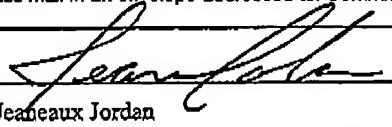
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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Toler Schaffer, LLP		
Signature			
Printed name	Jeffrey G. Toler		
Date	4-3-2007	Reg. No.	38,342

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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NO. 036 P. 3

APR 03 2007

Attorney Docket No.: 1970-0005C

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Darius Mostowfi, et al.

Title: SYSTEM AND METHOD FOR USING MULTIPLE COMMUNICATION PROTOCOLS IN MEMORY LIMITED PROCESSORS

App. No.: 10/613,656

Filed: July 3, 2003

Examiner: TSANG, Fan S.

Group Art Unit: 2161

Atty. Dkt. No.: 1970-0005C

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR STATUS OF APPLICATION**

Dear Sir:

Applicants hereby request a status of the above-referenced patent application. If a telephone call would expedite this request, please feel free to contact the undersigned.

Respectfully submitted,

4-3-2007  
\_\_\_\_\_  
Date

  
\_\_\_\_\_  
Jeffrey G. Toler, Reg. No. 38,342  
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